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N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Tadahiro OHMI et al.

Title:

SEMICONDUCTOR DEVICE

AND SEMICONDUCTOR
DEVICE MANUFACTURING

METHOD

Appl. No.:

10/553,416

International

4/13/2004

Filing Date:

371(c) Date:

10/17/2005

Examiner:

Thien F. Tran

Art Unit:

2811

Confirmation

1517

Number:

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the restriction requirement set forth in the Office Action mailed December 21, 2007, Applicant hereby provisionally elects Group I, Claims 1-12, for examination, without traverse.

Applicants reserve the right to file a divisional application to pursue the non-elected claims.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check or credit card payment form being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to

charge the unpaid amount to Deposit Account No. 19-0741. If any extensions of time are needed for timely acceptance of papers submitted herewith, Applicants hereby petition for such extension under 37 C.F.R. §1.136 and authorize payment of any such extensions fees to Deposit Account No. 19-0741.

Respectfully submitted,

Date

FOLEY & LARDNER LLP Customer Number: 22428 Telephone: (202) 945-6014

Facsimile:

(202) 672-5399

George C. Beck

Attorney for Applicant Registration No. 38,072

Thomas S. Belode 43,48